



DEPARTMENT OF THE AIR FORCE
HEADQUARTERS UNITED STATES AIR FORCE

MAR 8 2001

MEMORANDUM FOR: SAF/PAS
1690 Air Force Pentagon - 5D227
Washington DC 20330-1690

FROM: 
Francis G. Hinnant, Col, USAF
Associate Director of Acquisition
NPOESS Integrated Program Office
8455 Colesville Rd, Suite 1450
Silver Spring, MD 20910

SUBJECT: Low Wave Infrared, Photovoltaic, FPA Performance for
Remote Sensing Applications

Enclosed are the required ten (10) copies of the subject paper. This paper will be presented by Mr. Larry Dawson, Boeing Sensor Products Division, at the Society of Photo-Optical Instrumentation Engineers (SPIE) 15th Annual International Symposium on Aerospace/Defense Sensing, Simulation, and Controls (AEROSENSE) in Orlando, Florida, April 16-20, 2001. Your review is requested by 30 Mar 01.

The program office has reviewed the information and found it appropriate for public disclosure without change. The scientific information contained in this document is considered exempt from United States export control laws under International Traffic in Arms Regulations (ITAR) provision 125.4.b(3)

Point of contact on this matter is Maj Elisa Kang, NPOESS IPO/ADA at 301-427-2084 (Ext. 142).

cc: ADA (E. Kang)

Attachment: Presentation—10 copies

LWIR, Photovoltaic, $\text{Hg}_{1-x}\text{Cd}_x\text{Te}$, FPA Performance, for Remote Sensing Applications

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Abstract

Focal plane arrays (FPA's), used for remote sensing applications, are required to operate at high temperatures and are subject to high terrestrial background fluxes. Typical remote sensing applications like cloud/weather imagery, sea-surface temperature measurements, ocean color characterization, and land-surface vegetation indices also require FPA's that operate from the visible through the LWIR portion of the spectrum. This combination of harsh requirements have driven the design of a unique LWIR FPA, that operates at 80K under 300K background conditions, with an operating spectral range from 11.5 μm to 12.5 μm , and a spectral cutoff of 13.5 μm .

The FPA consists of 2 side by side arrays of 1x60 HgCdTe , (grown by molecular beam epitaxy) photovoltaic, detector arrays bump bonded to a custom CMOS Si readout. The 2 arrays are completely independent, and can be operated as such. The readout unit cell uses two, current-mode, analog building blocks; a Current Conveyor (CC1)¹ and a dynamic current mirror². The CC1 has input impedance below 300 Ohms and an injection efficiency that is independent of the detector characteristics. This combination extracts high performance and excellent sensitivity from detectors whose average RoA values are approximately 1.7 Ohm-cm² at T=80K. The dynamic current mirror is used to subtract high background photocurrent while preserving excellent dynamic range. In addition to the performance enhancing readout, the detectors are manufactured with integral microlenses and operated in reverse bias to take advantage of their increased dynamic impedance. The dark currents associated with reverse bias operation are subtracted along with the background photocurrents by the dynamic current mirror.

The expected and measured LWIR FPA performance will be presented. Measurements were performed on an LWIR FPA. Expected and measured FPA results are shown in the table below. The expected data are calculated from FPA models and compared to the measured values.

	PEAK DSTAR (Jones)	AVERAGE DSTAR (JONES)	MINIMUM RMS NOISE (Volts)	AVERAGE RMS NOISE (Volts)	RESPONSE (Volts)
CALCULATED	3.1E+11		961 μ V		1.8
MEASURED	>3.0E+11	1.88E+11	1mV	6.2mV	1.4

Table 1: Summary Results Table

1.0 INTRODUCTION

This paper describes the modeled and measured hybrid, LWIR, FPA performance. The FPA was designed for typical remote sensing applications.

2.0 DETECTOR TECHNOLOGY

The diode technology used to implement this focal plane array (FPA) is long wave infrared (LWIR), P on N, photovoltaic, Mercury Cadmium Telluride (HgCdTe), grown by molecular beam epitaxy (MBE) on CdZnTe substrates. The diodes have a spectral cutoff wavelength of $\lambda_c=13.5\mu\text{m}$, and have average RoA products of $1.7 \Omega\text{-cm}^2$ when operated at $T=80\text{K}$. The diodes are formed in two side-by-side linear arrays of 60 diodes each. Each diode has physical dimensions of $250\mu\text{m}$ in the long direction and $72.1\mu\text{m}$ in the narrow direction, for a total area of $1.80\text{E-}4 \text{ cm}^2$ (See Figure 3). Integral microlenses are produced on each diode to reduce the active area of the diode and thereby increase diode yield. The dimensions of the active area of each diode are $55\mu\text{m} \times 40\mu\text{m}$, for a total area of $2.20\text{E-}5 \text{ cm}^2$. Table 2 is a compilation of relevant detector parameters.

T (K)	80
RoA ($\Omega\text{-cm}^2$)	1.7
Cd (Farads)	1.08E-12
QE	.74
Detector Bias	-70mV
RdA ($\Omega\text{-cm}^2$)	170
Idark (-70mV) Amps	97.5nA
Phi Background (Ph/cm ² -s)	1.93E+15
Phi Max (Ph/cm ² -s)	2.80E+15
Microlens Area (cm ²)	1.80E-4
Active Area (cm ²)	2.20E-5

Table 2: Measured Detector Characteristics

3.0 AMPLIFIER ARCHITECTURE

The LWIR diodes have an average zero bias source resistance of $77k\ \Omega$. This low source resistance makes the LWIR diodes extremely difficult to interface to conventional direct injection (DI) and buffered direct injection (BDI) circuits. Both the DI and BDI suffer from low injection efficiency and lack of detector bias control. To overcome the shortcomings of the conventional architectures, an amplifier was designed with increased injection efficiency and reduced input impedance. The amplifier provides detector bias control. The amplifier has provisions for removing DC offsets from diode dark and background currents and suppresses low frequency detector noise.

Each detector amplifier unit cell consists of a current conveyor (CC1) followed by a dynamic current mirror and integration capacitor, the schematic is shown in Figure 1.

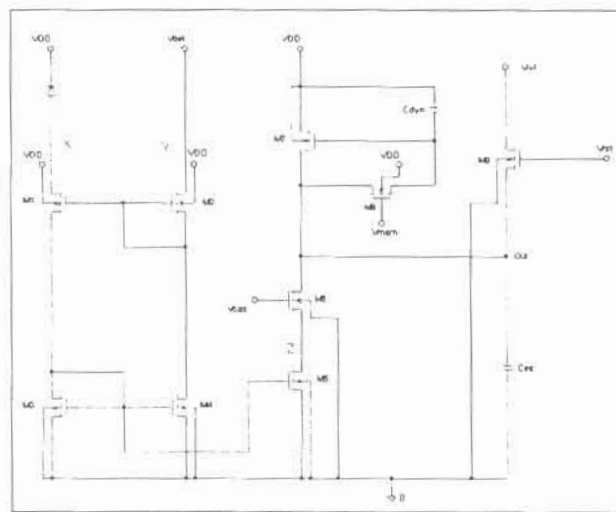


Figure 1: Unit Cell Amplifier Architecture (CC1)

The unit cell is followed by a sample and hold buffer amplifier, sample and hold, multiplexer amplifier, multiplexer and output buffer, a complete block diagram is shown in Figure 2.

The CC1 presents less than 300 Ohms input impedance to the diode, with an injection efficiency that is independent of the diode characteristics. Careful matching of the transistors M1-M5 can give an injection efficiency of 1^3 . The CC1 also provides accurate bias control for the diode. The dynamic current mirror restores dynamic range by subtracting off DC current components. The dynamic current mirror also reduces any low frequency detector noise components. The readout was fabricated at AMI using their standard $.6\mu\text{m}$ CMOS process. The layout of the complete IC is shown in Figure 3.

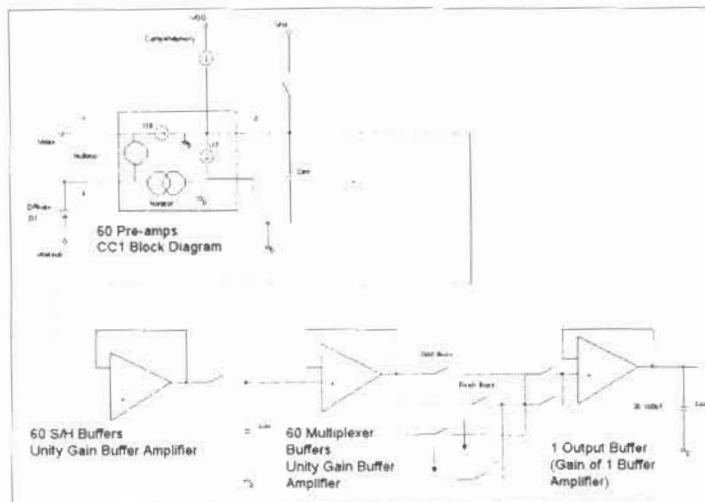


Figure 2: Single Analog Channel Block Diagram

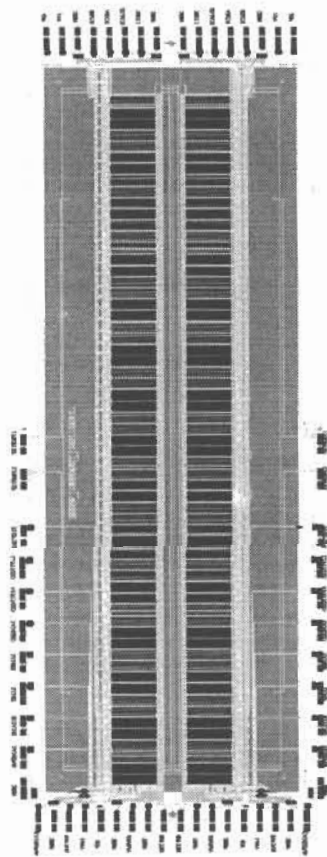


Figure 3: Layout of Detector Amplifier IC

5.0 HYBRID SIMULATION AND MEASURED RESULTS

The hybrid characteristics are compiled in Table 3. The schematic used for simulation and the simulation results are shown in Figures 1 and 4

Tint (sec)	47.76 μ s
Cint (Farads)	1.54pF
Vswing (Volts)	1.8 Volts
Full Well Capacity	19.3E+6 electrons
Snapshot S/H	Frame time equal Tint (95% duty cycle)
Cal Time	500 μ s
Interval between Cal	3s

Table 3: Amplifier Characteristics

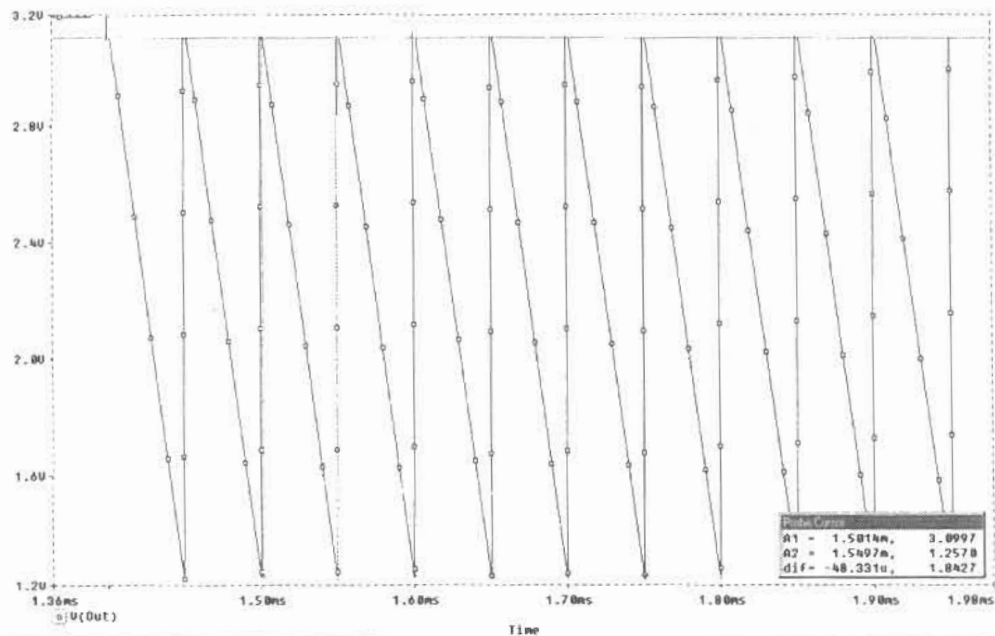


Figure 4: Simulation Results of Signal Integration

It was discovered via flux measurements on the Microlenses that they only transmit 28% of the incident light. All of the fluxes quoted in this text are adjusted for this reduction. The calculated hybrid performance characteristics are shown in Table 4. The measured amplifier noise floor and FPA total RMS noise are shown in Figures 5 and 6. The calculated value of total RMS noise, 960.55 μ V, compares very well with the minimum measured value of 1mV as shown in Figure 6. 36% of the pixels have total RMS noise less than or equal to 1E-3 Volts. The calculated value of peak Dstar, 3.13E+11 compares very well with the peak measured value of 3.9E11, as shown on the Dstar plot in Figure

7. 29% of the pixels have Dstars above $3.0\text{E}+11$ and 55% of the pixels have Dstars above $2.0\text{E}+11$. An NEI plot is shown in Figure 8. The minimum array NEI is $1.23\text{E}+12$, with an array average NEI of $1.95\text{E}+13$. Superimposed Oscilloscope plots of response are shown in Figure 9. The background level shown in the figure corresponds to a flux of $1.93\text{E}+15$ photons/cm²-s. The 2 other curves shown in Figure 9 correspond to fluxes of $2.15\text{E}+15$ and $3.47\text{E}+15$ Respectively. A measured response linearity plot is shown in Figure 10. The response is linear but quite non-uniform. No uniformity correction was applied to any of the Hybrid measurements.

Readout Thermal Noise	335.55 μV
Readout 1/f Noise	53.33 μV
Readout Switch Noise	29.94 μV
Total Amp Noise	341.1 μV
Detector Dark Shot Noise	535.89 μV
Photon Shot Noise	431.93 μV
Detector 1/f Noise	559.2 μV
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Peak Dstar	$3.13\text{E}+11$ Jones

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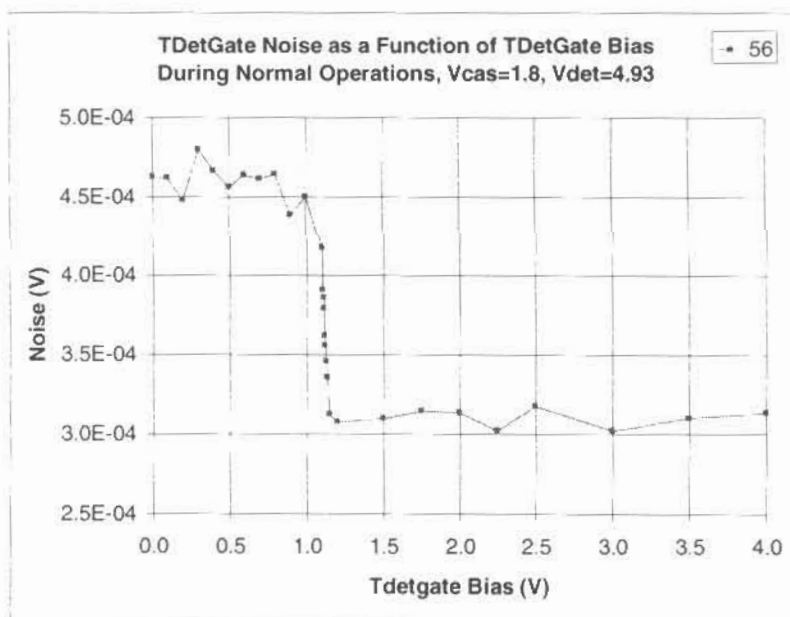


Figure 4: Measured Amplifier Noise Floor

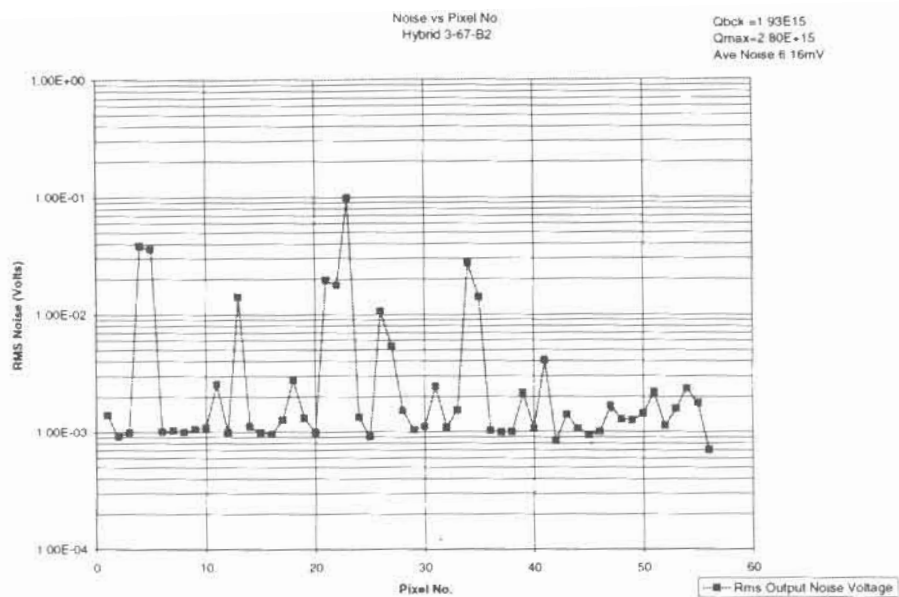


Figure 6: Measured Total RMS Noise for FPA

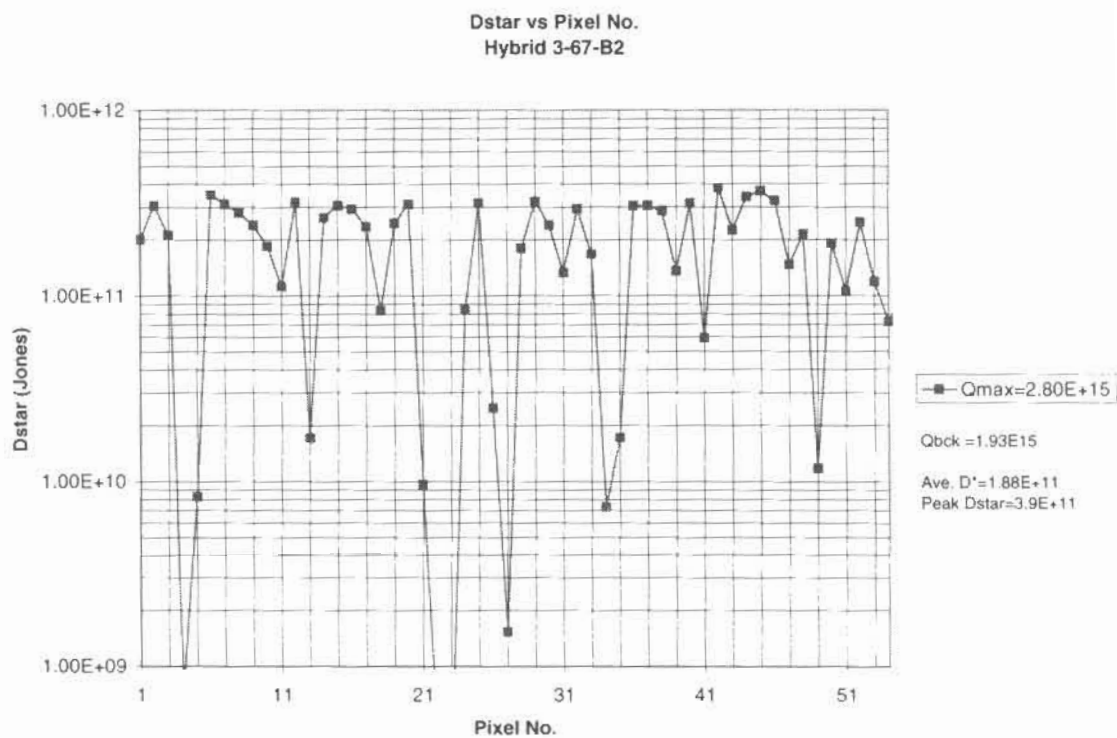


Figure 7: Measured Dstar Skyline Plot for FPA

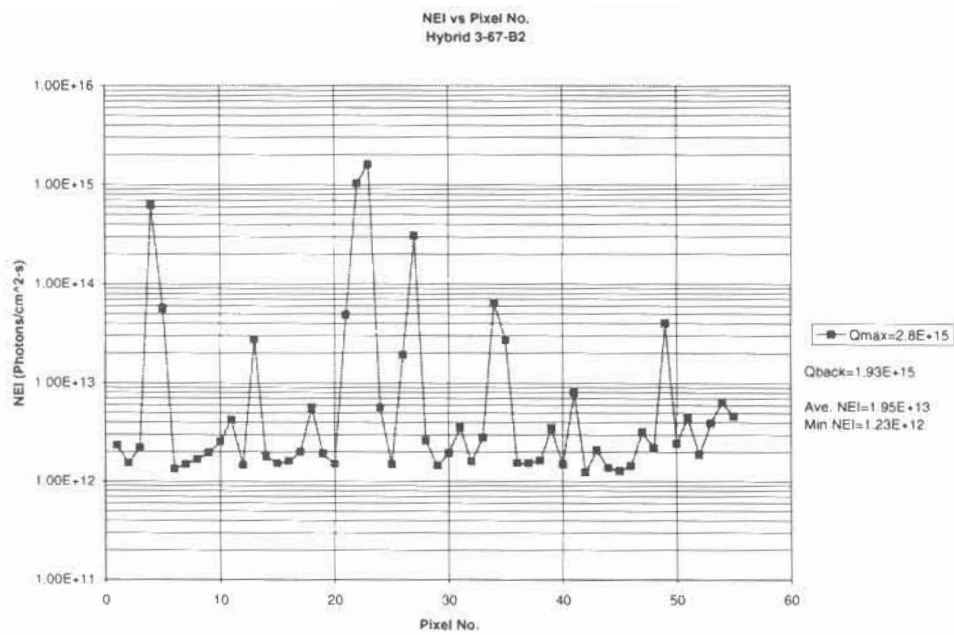


Figure 8: NEI vs. Pixel Number

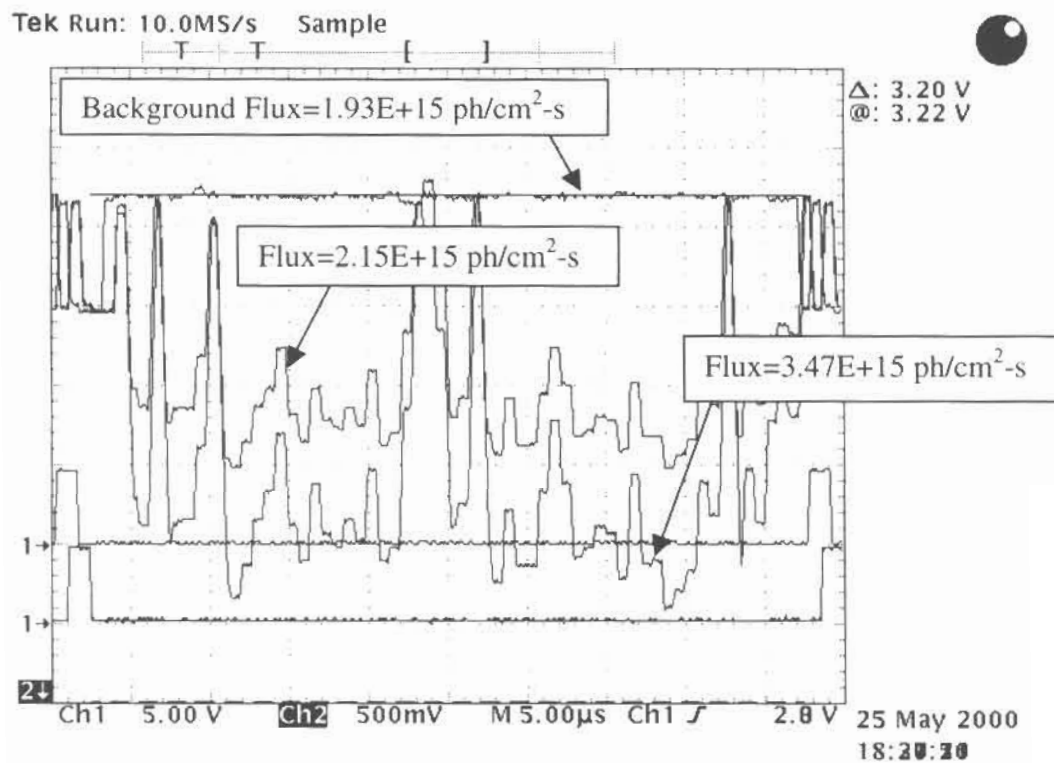


Figure 9: Oscilloscope Trace of Array Response

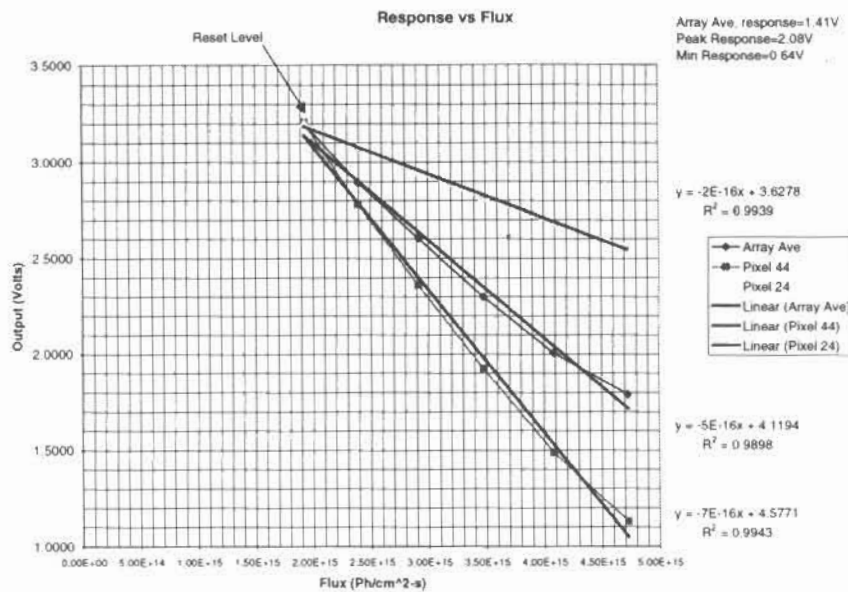


Figure 10: Measured Response

6.0 SUMMARY

A LWIR hybrid FPA was designed and manufactured for use in remote sensing applications. Simulated and measured results were compared.

REFERENCES

1. C. Toumazou, F.J. Lidgey and D.G. Haigh, "Analogue IC Design: the Current Mode Approach", Peter Peregrinus Ltd. On Behalf of the Institution of Electrical Engineers, pp. 94-99
2. C. Toumazou, F.J. Lidgey and D.G. Haigh, "Analogue IC Design: the Current Mode Approach", Peter Peregrinus Ltd. On Behalf of the Institution of Electrical Engineers, pp. 297-325
3. Nanyoung Yoon, Byunghyuck Kim, Hee Chul Lee, Hyungcheol Shin and Choong-Ki Kim, "A New Unit Cell of Current Mirroring Direct Injection Circuit for Focal Plane Arrays", SPIE, Vol. 3061, pp 93-101

TRANSMIT/ROUTE SLIP

7 Mar 01

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Frank DeLuccia	via e-mail		6 Mar 01
JR Svenson	via e-mail		6 Mar 01
Carol Welsh		CRP	8 Mar 01
Jeff Dedrick	via phone & e-mail		7 Mar 01
Elisa Kang			7 Mar 01
Col Hannant		1	7 Mar 01

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- | | |
|------------------------------|-------------------------------------|
| 1. APPROVAL/SIGNATURE | 9. YOUR INFORMATION |
| 2. CLEARANCE/INITIALS | 10. PER OUR CONVERSATION |
| 3. RECOMMENDATION OR COMMENT | 11. AS REQUESTED |
| 4. RETURN WITH MORE DETAILS | 12. NECESSARY ACTION |
| 5. INVESTIGATE AND REPORT | 13. CIRCULATE AMONG STAFF |
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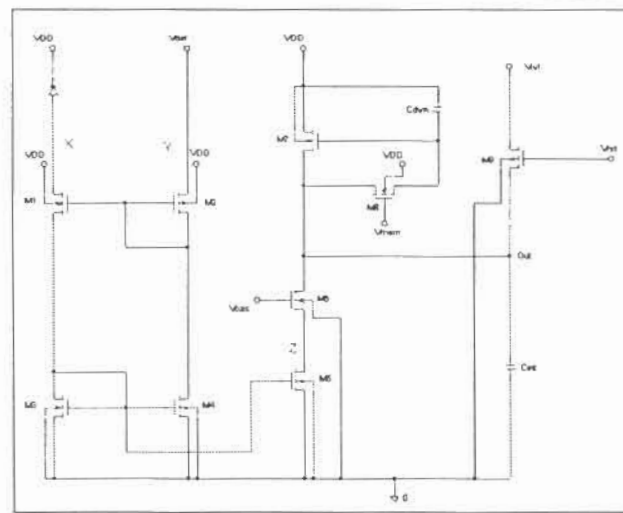


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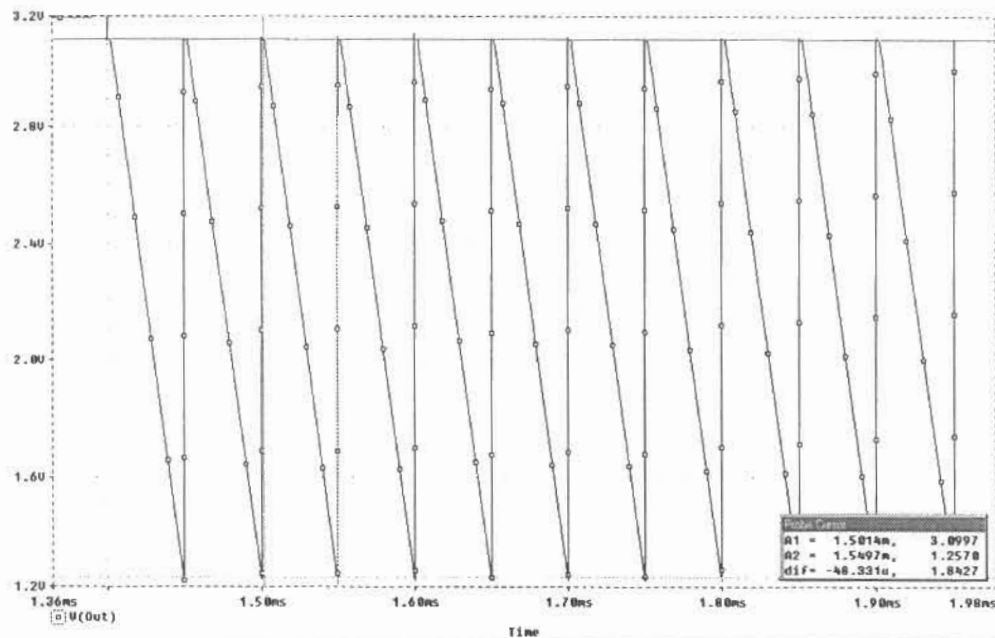


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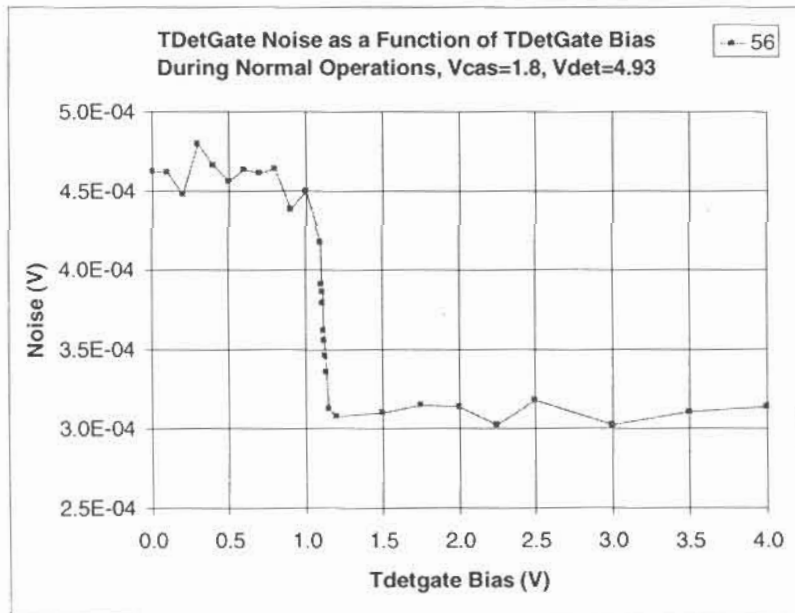


Figure 4: Measured Amplifier Noise Floor

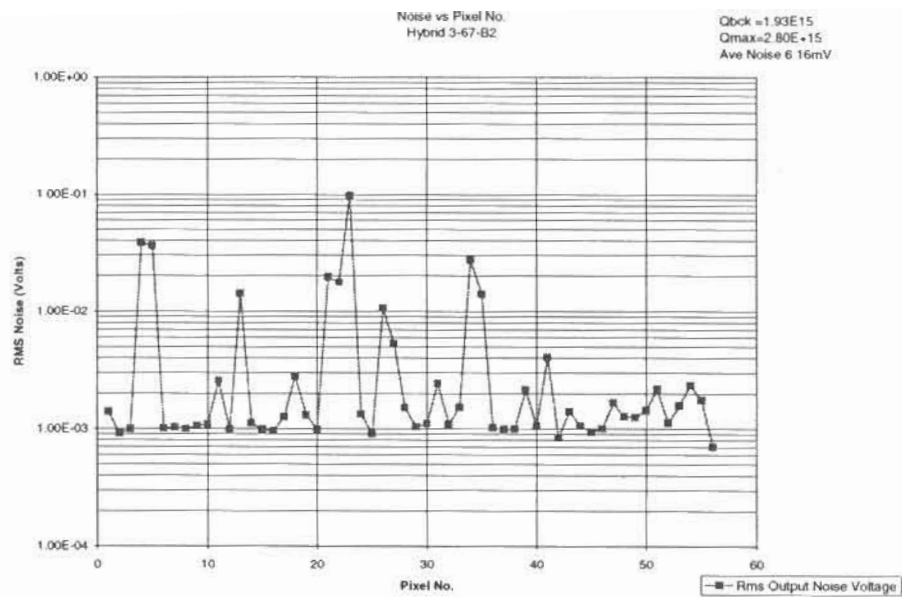


Figure 6: Measured Total RMS Noise for FPA

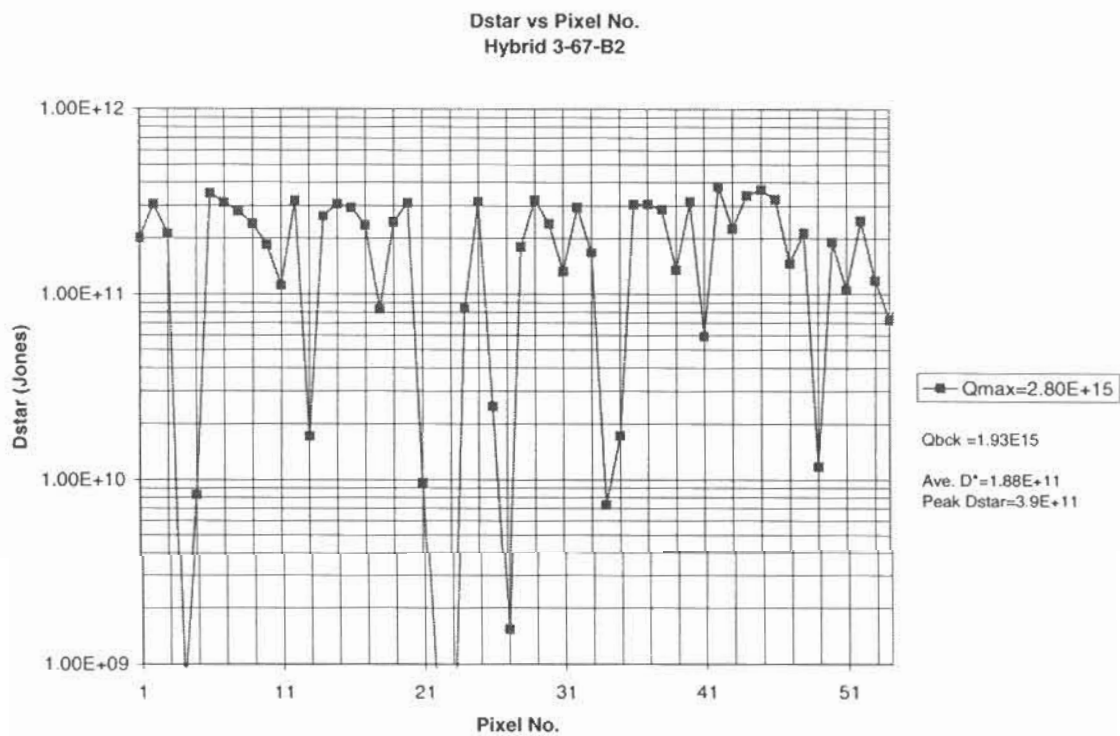


Figure 7: Measured Dstar Skyline Plot for FPA

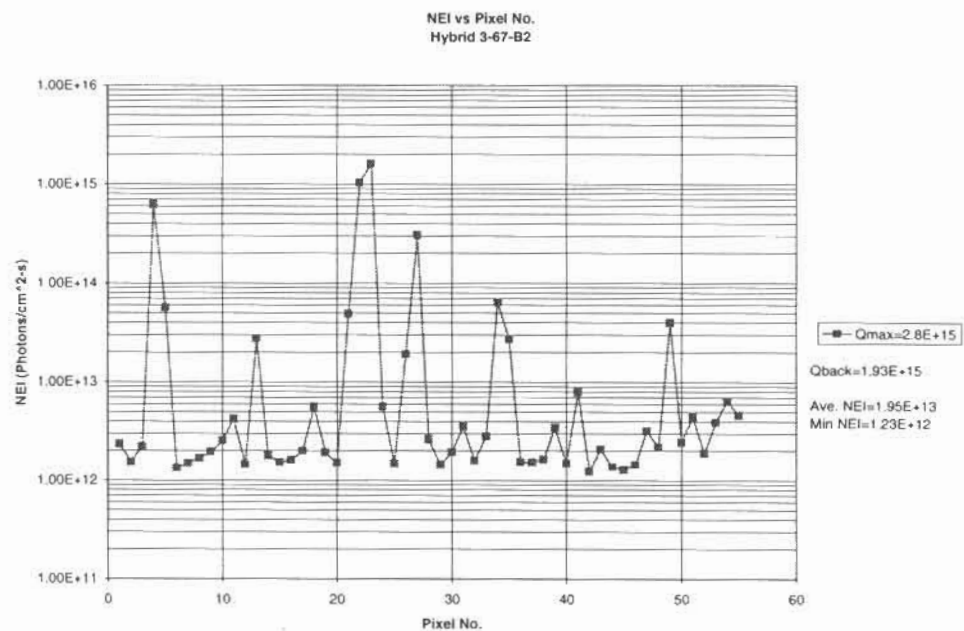


Figure 8: NEI vs. Pixel Number

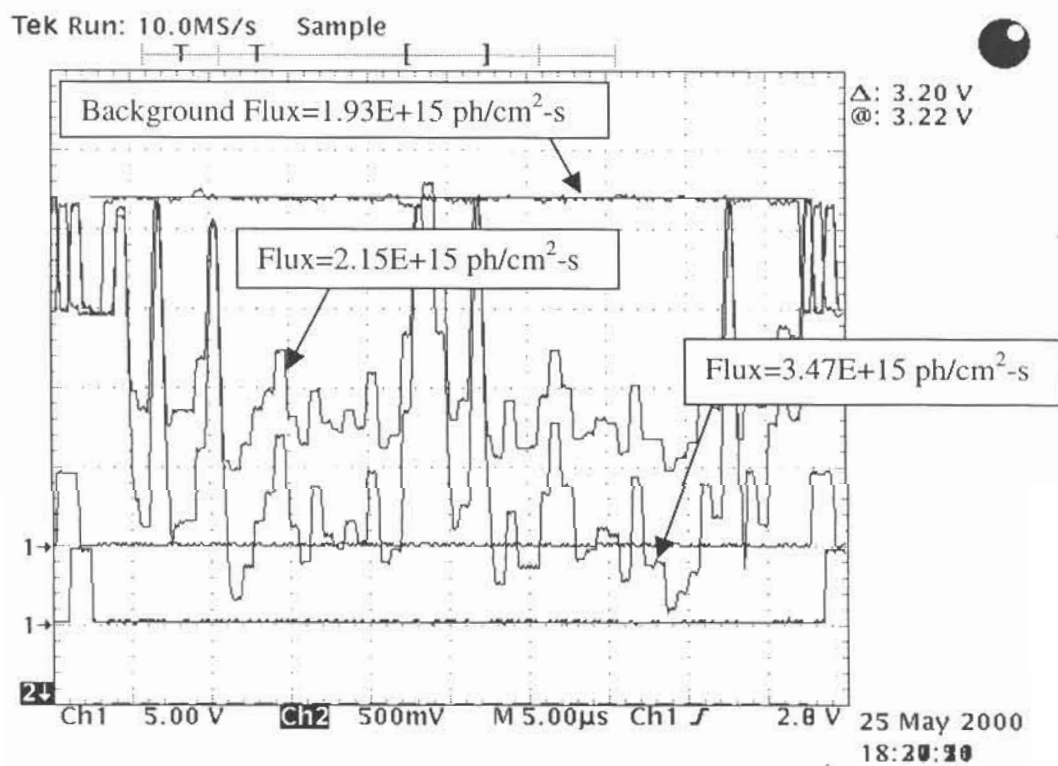


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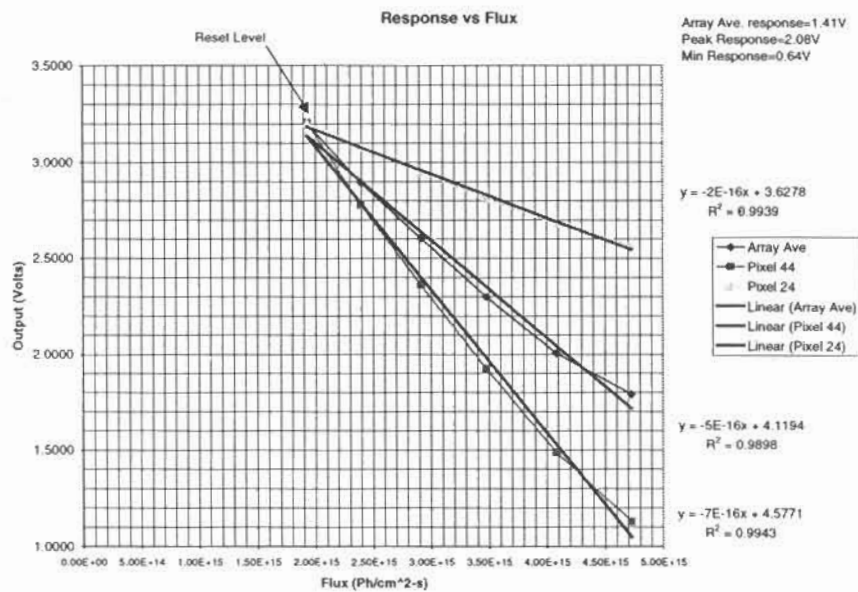


Figure 10: Measured Response

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